



Failing Data format for Embedded Memory

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Format of failing data

- Each failing data is composed of a set of data fields that is specific to a memory BIST controller and should have fixed length for each controller



Data Field

- User can customize data fields to include in failing data
 - `tstate`, `rw_state`, `dout`, `failmap`, `addr_reg`, `memnum`, `portnum`, `test_addr_shift`, `addr_reg_write` etc
- The set of data fields may change as design of bist controller evolves and new types of memory algorithm emerge.



Example of Failing Data

Preamble + **Tstate** + rw_state + **addr_reg** + failmap + **memnum** + Postamble

```
01111011101101101000000000111
```

```
01111111011011001001000000111
```

```
01100111011011010000001001111
```