



# Bitmap Processing in Virage Logic vision

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A graphic of a yellow microchip with a grid pattern, surrounded by several semi-transparent blue squares of varying sizes, some overlapping the chip.

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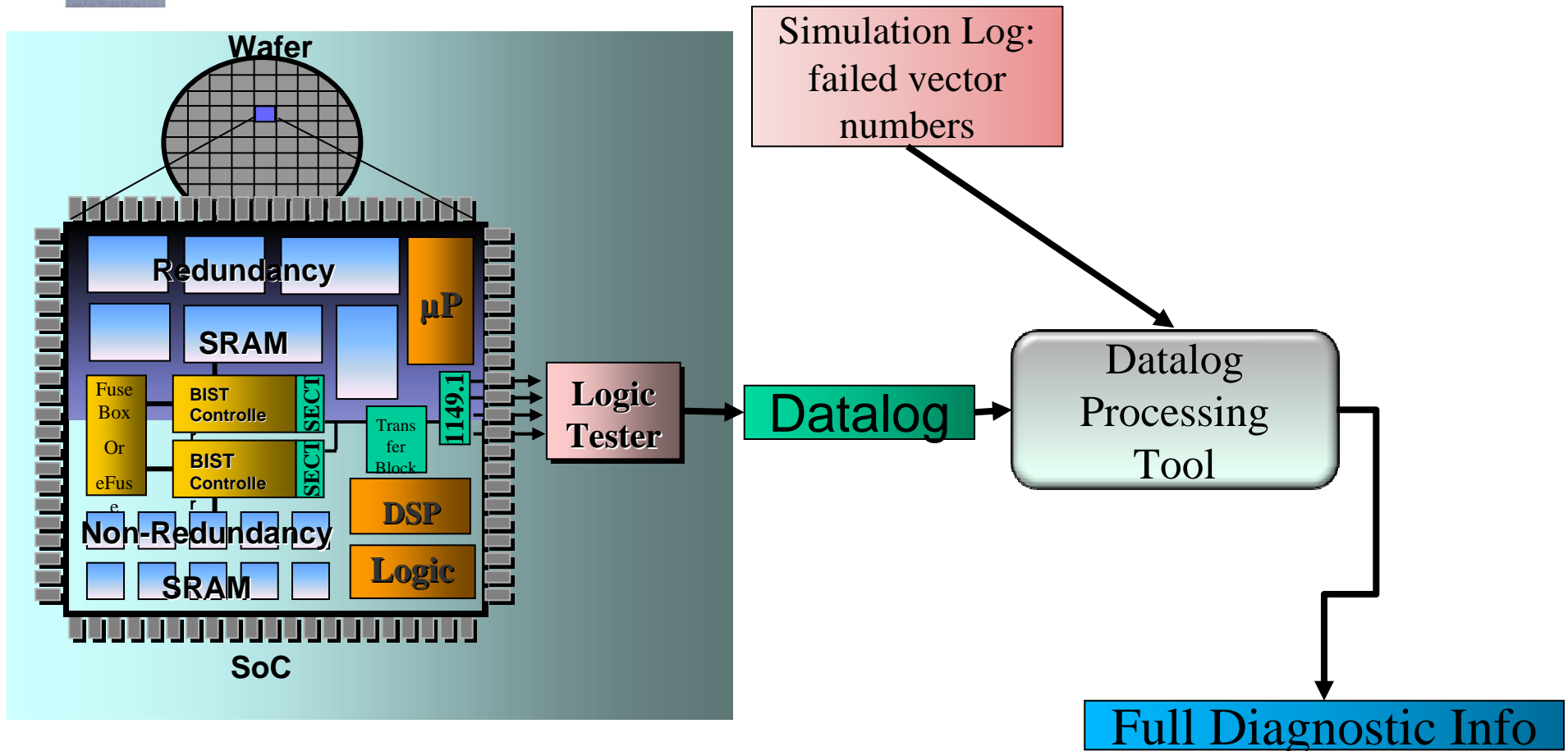
Virage Logic Datalog Processing

Datalog Information

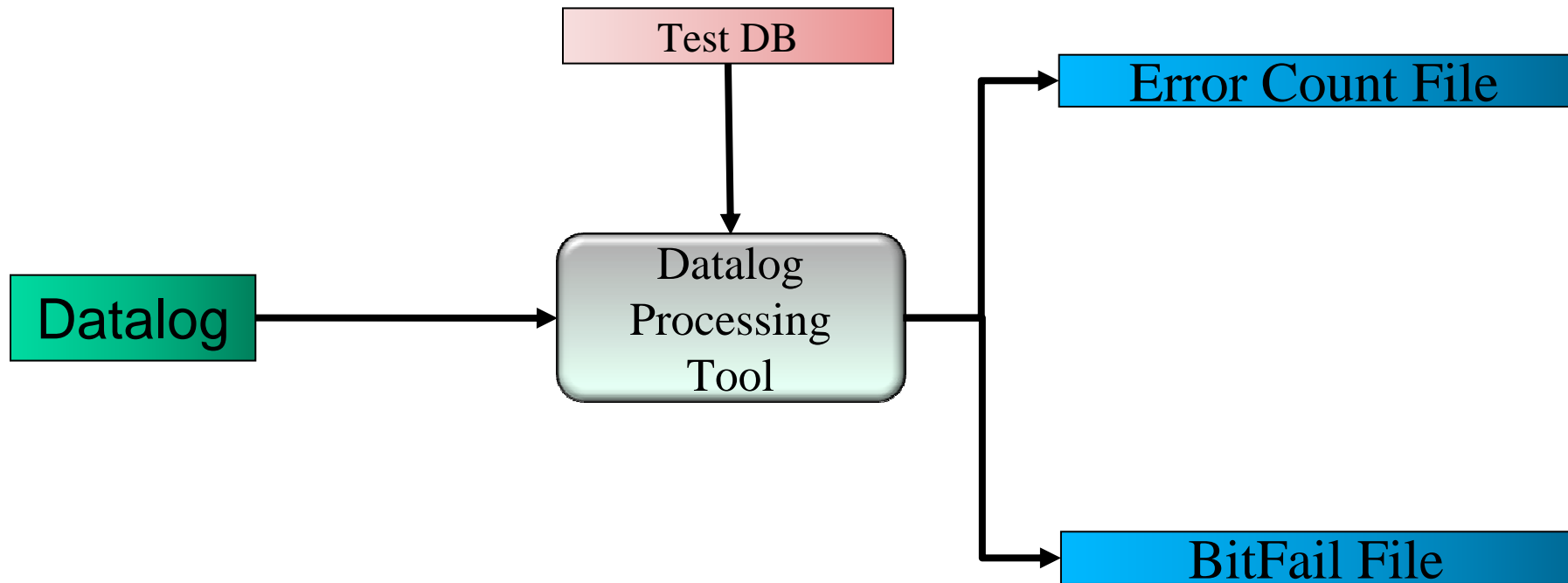
.Bitfail File Information

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# Embedded Memory Test Infrastructure and data log processing



# Virage Logic Datalog Processing





# Datalog Information

Lot Number

Device Code

Wafer Number

Date Stamp

Time Stamp

Wafer ID

Handler ID

....

Pattern Name

Flow Name

Failed Vector Numbers



# .Bitfail File Information

.Bitfail File contains the following information

- Product
- Lot ID
- PartBin X
- PartBin Y
- Pattern
- SMS
- Memory
- Flowpoint
- Logical row addr
- Logical column addr
- Physical row addr
- Physical col addr
- Physical X Coord
- Physical Y Coord
- I/O number
- Fail logic state
- Alg phase
- Port



# Error Count File Information

Error Count File contains of

- Pattern
- Lot ID
- SMS
- Memory
- Error Count per RAM
- Cg0
- cg1
- Rg0
- rg1